IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): A PC card having a connection plug connectable to an information processing apparatus, and a card connector through which an additional card can be connected, wherein multiple capabilities are added to said information processing apparatus by connecting said PC card to said information processing apparatus, said PC card comprising:

a communication control unit configured to control communication between said information processing apparatus and a function control unit of said PC card, [[and]] communication between said information processing apparatus and a function control unit of said additional card, and to recognize memory space of said PC card and memory space of said additional card; and

an area builder unit configured to analyze a card information structure of said additional card, build a global card information structure from the card information structure of said additional card recognize the location of an attribute area for storing card property information and card configuration information in the memory space of each of said PC card and said additional card, build a global card information structure area and a global register area in the attribute area of said PC card, locate the card information structure of said additional card in said global card information structure area together with a card information structure of said PC card, analyze card configuration information of said additional card, and build a global register area from the card configuration information of said additional card and perform an address control operation in order to locate the card configuration information of said additional card in said global register area together with a card configuration information of said PC card,

<u>wherein said area builder unit performs said address control operation when power is</u>

<u>supplied to said PC card, and said communication control unit transmits an initialization</u>

<u>completion notification signal, which notifies that said card information structure is ready for</u>

<u>reading, when said address control operation is completed</u>.

Claim 2 (Currently Amended): The PC card as claimed in claim 1, further

comprising:

an address control unit configured to switch address operations in order that a

functional register group of said PC card is located in an I/O area of said PC card when said

information processing apparatus outputs an I/O area control signal for accessing the function

of said PC card, and that a functional register group of said additional card is located in the

I/O area of said PC card when said information processing apparatus outputs an I/O area

control signal for accessing the function of said additional card.

Claim 3 (Currently Amended): The PC card as claimed in claim 1, further

comprising:

an interrupt request control unit that, when an interrupt request is received from the

function control unit of said PC card or the function control unit of said additional card, sets

information about said interrupt request in an interrupt cause register.

Claim 4 (Currently Amended): The PC card as claimed in claim 1, wherein the

function control unit of said PC card is provided with a wireless LAN communication

capability, [[and]]

the function control unit of said additional card is provided with a PHS

communication capability, and

said card configuration information is information required for activating said PC card and said additional card including a drive voltage level for said wireless LAN capability and PHS communication capability.

Claim 5 (Currently Amended): A method of controlling a PC card having a connection plug connectable to an information processing apparatus, and a card connector through which an additional card can be connected, wherein multiple capabilities are added to said information processing apparatus by connecting said PC card to said information processing apparatus, said method of controlling the PC card comprising:

a step of controlling communication between said information processing apparatus and a function control unit of said PC card, [[and]] communication between said information processing apparatus and a function control unit of said additional card, and recognizing the location of an attribute area for storing card property information an card configuration information in a memory space of each of said PC card and said additional card; and

a step of analyzing a card information structure of said additional card, building a global card information from the card information structure of said additional card building a global card information structure area and a global register area in the attribute area of said PC card, locate the card information structure of said additional card in said global card information structure area together with a card information structure of said PC card;[[,]] analyzing card configuration information of said additional card, and building a global register area from the card configuration information of aid additional card

performing an address control operation in order to locate the card configuration information of said additional card in said global register area together with a card configuration information of said PC card; and

transmitting an initialization completion notification signal, which notifies that said

card information structure is ready for reading, when said address control operation is

completed.

Claim 6 (Currently Amended): The method of controlling a PC card as claimed in

claim 5, further comprising:

a step of switching address operations in order that a functional register group of said

PC card is located in an I/O area of said PC card when said information processing apparatus

outputs an I/O area control signal for accessing the function of said PC card, and that a

functional register group of said additional card is located in the I/O area of said PC card

when said information processing apparatus outputs an I/O area control signal for accessing

the function of said additional card.

Claim 7 (Currently Amended): The method of controlling a PC card as claimed in

claim 5, further comprising:

a step of, setting information about interrupt request in an interrupt cause register,

when [[an]] said interrupt request is received from the function control unit of said PC card or

the function control unit of said additional card, setting information about said interrupt

request in an interrupt cause register.